

Integration of Various Types of Compensated Dielectric Bridges for mm Coplanar Applications

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This paper describes a new approach in the realization of integrated dielectric bridges for uniplanar technology. It shows how the electrical characteristics of a CPW are disturbed and how these parasitic effects may be compensated by simply modifying the strip width of the transmission line. Based on this approach several solutions are presented. Theoretical results (static analysis) and experimental results ("on wafer" measurements. 1 GHz - 50 GHz) are compared. In order to verify the efficiency of these dielectric bridges, filtering structures are tested.

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